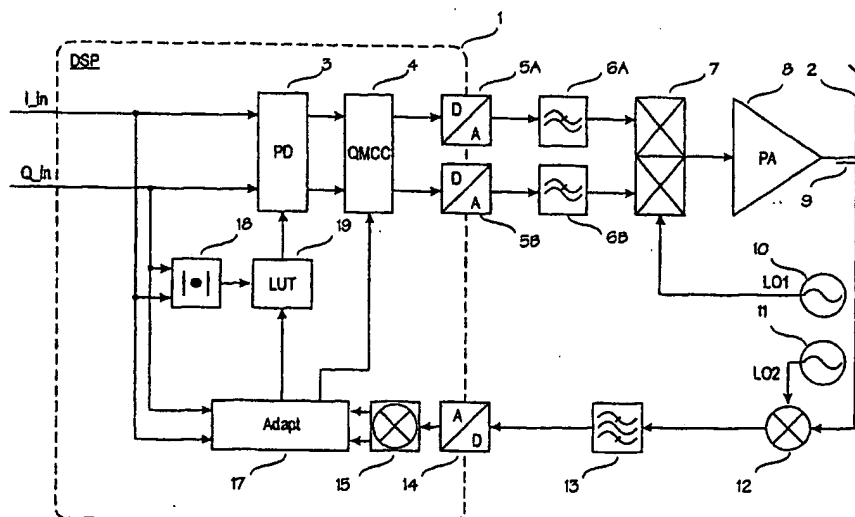




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁷ : H04L 27/36	A1	(11) International Publication Number: WO 00/59174
		(43) International Publication Date: 5 October 2000 (05.10.00)
(21) International Application Number: PCT/FI00/00246		(81) Designated States: AE, AG, AL, AM, AT, AT (Utility model), AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, CZ (Utility model), DE, DE (Utility model), DK, DK (Utility model), DM, DZ, EE, EE (Utility model), ES, FI, FI (Utility model), GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KR (Utility model), KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SK (Utility model), SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).
(22) International Filing Date: 24 March 2000 (24.03.00)		
(30) Priority Data: 990678 26 March 1999 (26.03.99) FI		
(71) Applicant (for all designated States except US): NOKIA NETWORKS OY [FI/FI]; Keilalahdentie 4, FIN-02150 Espoo (FI).		
(72) Inventor; and (75) Inventor/Applicant (for US only): RÄTTÖ, Mika [FI/FI]; Vanha Pisantie 12 A, FIN-02280 Espoo (FI).		
(74) Agent: KOLSTER OY AB; Iso Roobertinkatu 23, P.O. Box 148, FIN-00121 Helsinki (FI).		Published With international search report.

(54) Title: CORRECTION OF PHASE AND AMPLITUDE IMBALANCE OF I/Q MODULATOR



(57) Abstract

A method and an arrangement for determining correction parameters used for correcting phase and amplitude imbalance of an I/Q modulator in a transmitter, which transmitter comprises an I/Q modulator (7) for I/Q-modulating a signal to be transmitted and a corrector (4) for correcting the phase and amplitude imbalance caused by the I/Q modulator according to the correction parameters, the arrangement comprising means (9) for sampling the I/Q-modulated test signal to be transmitted, which is formed from I- and Q-test signals fed into the transmitter, means (14) for A/D-converting the signal samples taken from the test signal to be transmitted, means (15) for I/Q-demodulating the signal samples digitally into I- and Q-feedback signals, means (17) for determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals, and means (19) for determining the correction parameters of phase and amplitude on the basis of the determined phase and amplitude imbalance.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece			TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Italy	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Kenya	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	NZ	New Zealand		
CM	Cameroon			PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	KZ	Kazakstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	LI	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EE	Estonia	LR	Liberia	SG	Singapore		

CORRECTION OF PHASE AND AMPLITUDE IMBALANCE OF I/Q MODULATOR

BACKGROUND OF THE INVENTION

The invention relates to correction of phase and amplitude imbalance caused by an I/Q modulator in a transmitter.

5 In new radio systems, the scarcity of radio frequencies makes it necessary to use spectrum-efficient modulation methods. In Europe, a new radio system standard has been developed for PMR (Professional Mobile Radio) users, called TETRA (Terrestrial Trunked Radio). $\pi/4$ -DQPSK ($\pi/4$ -shifted Differential Quadrature Phase Shift Keying) has been selected as the modulation method of the system. As far as a transmitter is concerned, a drawback to the modulation method is the variation in the amplitude of the envelope of a radio frequency signal, which causes InterModulation (IM) in a nonlinear amplifier. The IM results spread the spectrum of a transmitted signal and thus tend to reduce the gain obtained from using the linear modulation method. The IM results cannot usually be filtered since they are formed extremely close to the desired signal. With constant-amplitude modulation methods, no spreading of the spectrum occurs; therefore, the signal can be amplified by a nonlinear amplifier.

20 A trunked PMR system, wherein different user groups share the same radio channels, has stringent requirements regarding adjacent channel interference caused by a transmitter. These requirements necessitate good linearity in the transmitter of the radio system used.

In a power amplifier, good linearity is only achieved with poor efficiency. However, the efficiency of portable equipment should be as high as possible for the operation time to be sufficient and in order not to waste battery capacity. In addition, at least relatively good efficiency is required of power amplifiers at base stations in order to avoid cooling problems. Sufficient efficiency and linearity can only be achieved by linearizing the transmitter.

30 If the nonlinearities of an amplifier were known in advance, it would be possible to form inverse functions of the nonlinearities to convert the input signal, whereby the nonlinearities would be cancelled. The characteristics of the amplifier do not, however, stay the same but they change due to, for example, aging, warming up, and according to the radio channel and power level used. In addition, amplifiers have individual differences. Linearization methods are needed that are capable of adjusting adaptively to changing conditions.

Research has been conducted on many different linearization methods, and three have been found to possess characteristics suitable for practical radio systems. These methods are feedforward, cartesian feedback and predistortion. A linearization method can also be adaptive.

5 Thus, if the nonlinear transfer function of the amplifier is known and if it does not vary as a function of time, the signal to be transmitted can be linearized by applying to the signal a suitable transfer function causing pre-distortion. Hence, the signal outputted from the amplifier can be made linear. This method is called predistortion. At baseband, for example, predistortion
10 can be carried out by using a lookup table (LUT) into which are stored conversion parameters causing predistortion, i.e. predistortion parameters, in which case the conversion parameters to be used at a given time are selected on the basis of the amplitude of the signal to be predistorted.

 In a predistortion system based on amplitude, distortion of the amplitude of a signal is assumed to be independent of the phase of the signal. An
15 I/Q modulator in the transmitter, however, causes distortions dependent on the phase of the signal, which impoverish the performance of the predistortion system. An I/Q modulator operates on a quadrature modulation basis. It enables two independent signals to be combined in the transmitter and be
20 transmitted on the same transmission band and the signals to be separated again at the receiver. The principle of quadrature modulation is that two separate signals, I and Q (Inphase and Quadrature phase), are modulated by using the same carrier wave frequency, but the phases of the carrier waves differ from each other in that the carrier wave of signal Q lags 90° the carrier wave
25 of signal I. After modulation, the signals are summed. Thanks to the phase difference, the I- and Q-signals can be separated from each other when the sum signal is demodulated. The distortions cause errors in the mutual phase and amplitude balance of the I- and Q-signals. Typically, the amplitude imbalance caused by the I/Q modulator is few per cents (2 to 5%) and the angle
30 error 2 to 3 degrees. Correction circuits with fixed correction parameters set therein have been used in connection with predistortion systems for correcting the distortion caused by the I/Q modulator. The problem is then manufacturability since each I/Q modulator requires unique parameters which must be set as early as during the production. In addition, the magnitude of the phase
35 and amplitude imbalance caused by the I/Q modulator depends on the frequency, in which case when the frequency is changed, the fixedly-set correc-

tion does not necessarily work any longer. Another known solution has been disclosed in "New Methods for Adaptation of Quadrature Modulators and Demodulators in Amplifier Linearization Circuits" by Cavers, J. K. in IEEE Transactions on Vehicular Technology, Vol. 46, No. 3, August 1997, pp. 707 to 716.

- 5 It discloses a system wherein the correction parameters are determined by comparing the output of an I/Q modulator with the input thereof. The solution requires, however, an additional feedback branch independent of the feedback branch of the predistorter, which results in a complex arrangement.

BRIEF DESCRIPTION OF THE INVENTION

- 10 An object of the invention is thus to provide a method and an arrangement implementing the method such that the above-mentioned problems can be solved. The objects of the invention are achieved by a method of determining correction parameters used for correcting the phase and amplitude imbalance of an I/Q modulator in a transmitter, which transmitter comprises an
- 15 I/Q modulator for I/Q-modulating a signal to be transmitted and a corrector for correcting the phase and amplitude imbalance caused by the I/Q modulator according to the correction parameters, which method comprises the steps of feeding an I- and Q-test signal into the transmitter, and sampling the I/Q-modulated test signal to be transmitted, the method being characterized by
- 20 further comprising the steps of A/D-converting the signal samples taken from the test signal to be transmitted, I/Q-demodulating the signal samples digitally into I- and Q-feedback signals, determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals, and determining the correction parameters of phase and amplitude on
- 25 the basis of the determined phase and amplitude imbalance.

- The invention is based on the idea that the I/Q demodulation of the feedback signal is carried out digitally, whereby the I/Q demodulation does not cause phase or amplitude imbalance for the signal. The phase and amplitude imbalance caused by the I/Q modulator, and thus the correction parameters of
- 30 correction feedback, can then be determined by means of the feedback signals since the feedback signals comprise phase and amplitude errors caused only by the I/Q modulator. An advantage of the method of the invention is that the feedback arrangement is simple. Furthermore, the same feedback branch can be used for determining the parameters of the predistorter and the cor-
- 35 rector for the I/Q modulator. This considerably simplifies the structure of the

predistortion system of the transmitter and, as fewer components are needed, also saves costs.

The invention further relates to an arrangement for determining correction parameters used for correcting phase and amplitude imbalance of an I/Q modulator in a transmitter, which transmitter comprises an I/Q modulator for I/Q-modulating a signal to be transmitted and a corrector for correcting the phase and amplitude imbalance caused by the I/Q modulator according to the correction parameters, which arrangement comprises means for sampling the I/Q-modulated test signal to be transmitted, which is formed from I- and Q- test signals fed into the transmitter, the arrangement being characterized by further comprising means for A/D-converting the signal samples taken from the test signal to be transmitted, means for I/Q-demodulating the signal samples digitally into I- and Q-feedback signals, means for determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals, and means for determining the correction parameters of phase and amplitude on the basis of the determined phase and amplitude imbalance. Such an arrangement enables the advantages of the method of the invention to be achieved by a simple structure.

Preferred embodiments of the invention are disclosed in the dependent claims.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention is now described in closer detail in connection with the preferred embodiments and with reference to the accompanying drawings, in which

Figure 1 is a block diagram of a transmitter of the invention according to a preferred embodiment thereof, and

Figure 2 shows, on an I/Q-plane, distortions caused by an I/Q modulator to a signal.

DETAILED DESCRIPTION OF THE INVENTION

Figure 1 is a block diagram of a transmitter of the invention according to a preferred embodiment thereof. It is to be noted that the figure shows only elements relevant for the invention to be understood. In the figure, I- and Q-signals I_IN and Q_IN to be transmitted are supplied to the transmitter. Predistortion carried out to cancel the nonlinearity of an amplifier 8 as well as correction of the phase and amplitude imbalance caused by an I/Q modulator 7

are implemented by a digital signal processor (DSP) 1. Predistortion and correction could also be implemented by e.g. an ASIC (Application-Specific Integrated Circuit) circuit; this, however, is irrelevant as far as the basic idea of the invention is concerned. Predistortion is carried out at a predistorter 3 according to correction parameters obtained from a lookup table 19. The predistorted signals are corrected at a correcting unit 4 of the I/Q modulator in order to compensate for the phase and amplitude imbalance caused by the I/Q modulator 7. D/A converters 5A and 5B D/A-convert the signals obtained from the correcting unit 4 into analogue signals, which are also preferably filtered by low-pass reconstruction filters 6A and 6B. These signals are further supplied to the I/Q modulator 7. The I/Q modulator 7 and an I/Q demodulator 15 operate on a quadrature modulation basis, which enables two independent signals to be combined at the transmitter and to be transmitted on the same transmission band, and the signal to be separated again at the receiver. The principle of quadrature modulation is that two separate signals, I and Q (Inphase and Quadrature phase), are modulated by using the same carrier wave frequency, but the phases of the carrier waves differ from each other in that the carrier wave of signal Q lags 90° the carrier wave of signal I. After modulation, the signals are summed. Thanks to the phase difference, the signals I and Q can be separated from each other when the sum signal is demodulated. The signals are modulated and combined at the I/Q modulator 7. The I/Q modulator operates synchronized by a local oscillator 10. The I/Q-modulated signal is supplied to a power amplifier PA8 and further to an antenna 2 to be transmitted. Simultaneously, feedback is formed by means of a sampling arrangement 9, such as a directional coupler. The radio frequency (e.g. 400 MHz) feedback signal is down-mixed to an intermediate frequency of 450 kHz, for example, preferably by a down-mixer 12. Down-mixing is carried out synchronized by a local oscillator 11. The intermediate-frequency signal can, if necessary, be filtered by a broadband filter 13, and also attenuated. The intermediate-frequency signal is sampled by an A/D converter 14 for baseband (or intermediate frequency) processing. This is preferably carried out by using undersampling (sampling frequency 162 kHz, for example); this, however, is irrelevant as far as the basic idea of the invention is concerned. Undersampling means that the signal to be sampled is sampled at a lower frequency than the Nyquist frequency. As is known, undersampling can be used if the band of the signal to be sampled is sufficiently restricted. The advantage of using undersampling

in the A/D conversion 14 is, for example, that less memory is needed in the conversion than in normal sampling (or oversampling). In addition, the use of undersampling does not usually require additional procedures since the band of the signal supplied to the A/D converter is typically already restricted. The
5 band of the signal can be restricted, if necessary, by potential passband filtering 13.

The I/Q demodulator 15 is implemented by a digital signal processor 1 (or by an ASIC circuit, for example). The A/D-converted intermediate-frequency feedback signal is I/Q-demodulated by digitally multiplying into
10 baseband I- and Q-feedback signals by the I/Q demodulator 15. The I/Q demodulator is thus implemented by software at the digital signal processor 1. The digitally implemented I/Q demodulator 15 does not cause distortions to the signal, as an analogue I/Q demodulator would. The baseband I- and Q-feedback signals are conveyed to a computing unit 17 wherein the predistortion table 19 is generated. The manner in which the predistortion table 19 is
15 generated is irrelevant to the invention. The computing unit 17 also receives signals I_IN and Q_IN supplied to the transmitter to be transmitted. The actual predistortion is carried out by means of the predistortion table 19. At a unit 18, an absolute value, i.e. the amplitude of the signal, is determined from a complex signal comprising signals I_IN and Q_IN. This amplitude data is fed to the
20 amplitude predistortion table 19, which, on the basis of the amplitude data, gives the corresponding correction parameters to the predistorter 3. Being irrelevant to the basic idea of the invention, the predistortion method used may differ from the method disclosed above.

25 Regardless of how the above-described predistortion carried out to linearize the amplifier 8 has been implemented, the determination and correction according to the invention of the phase and amplitude imbalance caused by the I/Q modulator 7 is carried out in the following manner: a constant-envelope test signal, e.g. a sine signal, is fed to the transmitter such that there
30 is a 90° phase shift between the signals of I- and Q-branches. The amplifier 8 is then retained in the linear operation range, in which case it does not distort the test signal travelling therethrough. Furthermore, when the digital I/Q demodulation 15 is used, which, as was already stated above, does not cause distortions to the signal, the phase and amplitude errors determined from the I-
35 and Q-feedback signals supplied to the computing unit 17 are distortions caused by the I/Q modulator 7. Figure 2 shows, on an I/Q-plane, a circle 20

formed from an ideal sine signal sequence (or an average of several sequences). The I/Q-plane refers to a common representation form of the I- and Q- signals wherein the values of the I- and Q-signals are presented by means of I- and Q-axes orthogonally positioned in regard to each other. The distortions caused by the I/Q modulator have been illustrated by an ellipse 21. It is to be noted that in the figure, the distortions have been exaggerated for an easier understanding. At the computing unit 17, the ellipse 21 corresponding to one signal sequence is determined from the I- and Q-feedback signals. The ellipse 21 can also be determined from several signal sequences by averaging, for example. Next, the distorted I- and Q-axes corresponding to the feedback signals are determined on the I/Q-plane by means of timing information: since the delay caused by feedback for the signal is known, and, on the other hand, the test signal fed to the transmitter is also known, the distorted I- and Q-axes can be determined from the I- and Q-feedback signals by means of this data. Next, the diameters of the ellipse 21 are measured in the direction of the distorted I- and Q-axes. Amplitude imbalance causes the ideal circle 20 to be stretched into the ellipse 21, whereby the amplitude imbalance can be determined from the relation between the diameters measured in the direction of the distorted I- and Q-axes. Phase imbalance, in turn, changes the angle between the I- and Q-axes, whereby the phase imbalance can be determined on the basis of the angle between the distorted I- and Q-axes. In other words, the magnitude of the phase imbalance is the angular value whose magnitude corresponds to the magnitude by which the angle between the distorted I- and Q-axes differs from the phase shift between the I- and Q-test signals.

When the phase and amplitude imbalance have been determined, the corresponding correction parameters are determined at the computing unit 17 and fed to the correcting unit 4. The form of the correction parameters is irrelevant to the invention, and so is the precise operation of the correcting unit 4. The correcting unit 4 can be symmetrical, in other words it corrects the phase and amplitude imbalance of both the I- and Q-branches. In such a case, there are typically four correction parameters: the correction parameters of the phase and amplitude of the I- and Q-branches. The correcting unit 4 can also be asymmetrical, in which case it corrects either the I-signal or the Q-signal only. The imbalance of the I- and Q-branch can be removed also by means of the asymmetrical correcting circuit 4, whereby only two correction parameters are needed, which reduces the need for calculation. A drawback to the asym-

metrical correction is that the total amplitude of the signal may change as a result of the correction. By using the symmetrical correction circuit 4, the total amplitude does not change as a result of the correction.

The correction parameters can be updated at certain predetermined intervals, for example, or in response to a parameter or to an external request when, for example, the frequency used or some such factor changes. The TETRA system comprises a special linearization time slot reserved for linearizing the transmitter. Hence, in the TETRA system, it is during the linearization time slot when the correction parameters of the I/Q modulator 7 can be preferably determined.

Although the use of the invention has been described herein mostly in connection with the TETRA system, there are no restrictions to the use of the invention in systems of other type as well. The structure of the transmitter used may differ from the described one while the basic idea of the invention remains the same. It is obvious to those skilled in the art that the basic idea of the invention can be implemented in many ways as technology progresses. The invention and the embodiments thereof are thus not restricted to the examples described above but they can vary within the scope of the claims.

CLAIMS

1. A method of determining correction parameters used for correcting phase and amplitude imbalance of an I/Q modulator in a transmitter, which transmitter comprises an I/Q modulator for I/Q-modulating a signal to be transmitted and a corrector for correcting the phase and amplitude imbalance caused by the I/Q modulator according to the correction parameters, which method comprises the steps of
- 5 feeding an I- and Q-test signal into the transmitter, and
sampling the I/Q-modulated test signal to be transmitted, the
10 method being **characterized** by further comprising the steps of
A/D-converting the signal samples taken from the test signal to be transmitted,
I/Q-demodulating the signal samples digitally into I- and Q-feedback signals,
- 15 determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals, and
determining the correction parameters of phase and amplitude on the basis of the determined phase and amplitude imbalance.
2. A method as claimed in claim 1, **characterized** by determining the correction parameters at predetermined intervals.
- 20 3. A method as claimed in claim 1, **characterized** by determining the correction parameters during a linearization time slot.
4. A method as claimed in claim 1, 2 or 3, **characterized** by the I- and Q-test signals being sine signals having a phase shift of 90° between them.
- 25 5. A method as claimed in claim 4, **characterized** by the step of determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals comprising the steps of:
- 30 determining an ellipse formed by one sequence or an average of several sequences of the I- and Q-feedback signals on an I/Q-plane,
determining, on the basis of timing information, the distorted I- and Q-axes corresponding to the I- and Q-feedback signals on the I/Q-plane,
measuring the diameters of the ellipse in the direction of the distorted I- and Q-axes,
- 35

determining the amplitude imbalance from the relation between the diameters, and

determining the phase imbalance from the angle between the distorted I- and Q-axes.

5 6. An arrangement for determining correction parameters used for correcting phase and amplitude imbalance of an I/Q modulator in a transmitter, which transmitter comprises an I/Q modulator (7) for I/Q-modulating a signal to be transmitted and a corrector (4) for correcting the phase and amplitude imbalance caused by the I/Q modulator according to the correction parameters,
10 which arrangement comprises

 means (9) for sampling the I/Q-modulated test signal to be transmitted, which is formed from I- and Q-test signals fed into the transmitter, the arrangement being **characterized** by further comprising

 means (14) for A/D-converting the signal samples taken from the
15 test signal to be transmitted,

 means (15) for I/Q-demodulating the signal samples digitally into I- and Q-feedback signals,

 means (17) for determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals,
20 and

 means (17) for determining the correction parameters of phase and amplitude on the basis of the determined phase and amplitude imbalance.

 7. An arrangement as claimed in claim 6, **characterized** in that the arrangement is arranged to determine the correction parameters at
25 predetermined intervals.

 8. An arrangement as claimed in claim 6, **characterized** in that the arrangement is arranged to determine the correction parameters during a linearization time slot.

 9. An arrangement as claimed in claim 6, 7 or 8, **character-**
30 **ized** in that the I- and Q-test signals are sine signals having a phase shift of 90° between them.

 10. An arrangement as claimed in claim 9, **characterized** in that the means for determining the phase and amplitude imbalance caused by the I/Q modulator on the basis of the I- and Q-feedback signals are arranged
35 to

- determine an ellipse (21) formed by one sequence or an average of several sequences of the I- and Q-feedback signals on an I/Q-plane,
- determine, on the basis of timing information, the distorted I- and Q-axes corresponding to the I- and Q-feedback signals on the I/Q-plane,
- 5 measure the diameters of the ellipse (21) in the direction of the distorted I- and Q-axes,
- determine the amplitude imbalance from the relation between the diameters, and
- determine the phase imbalance from the angle between the dis-
- 10 torted I- and Q-axes.

II
III

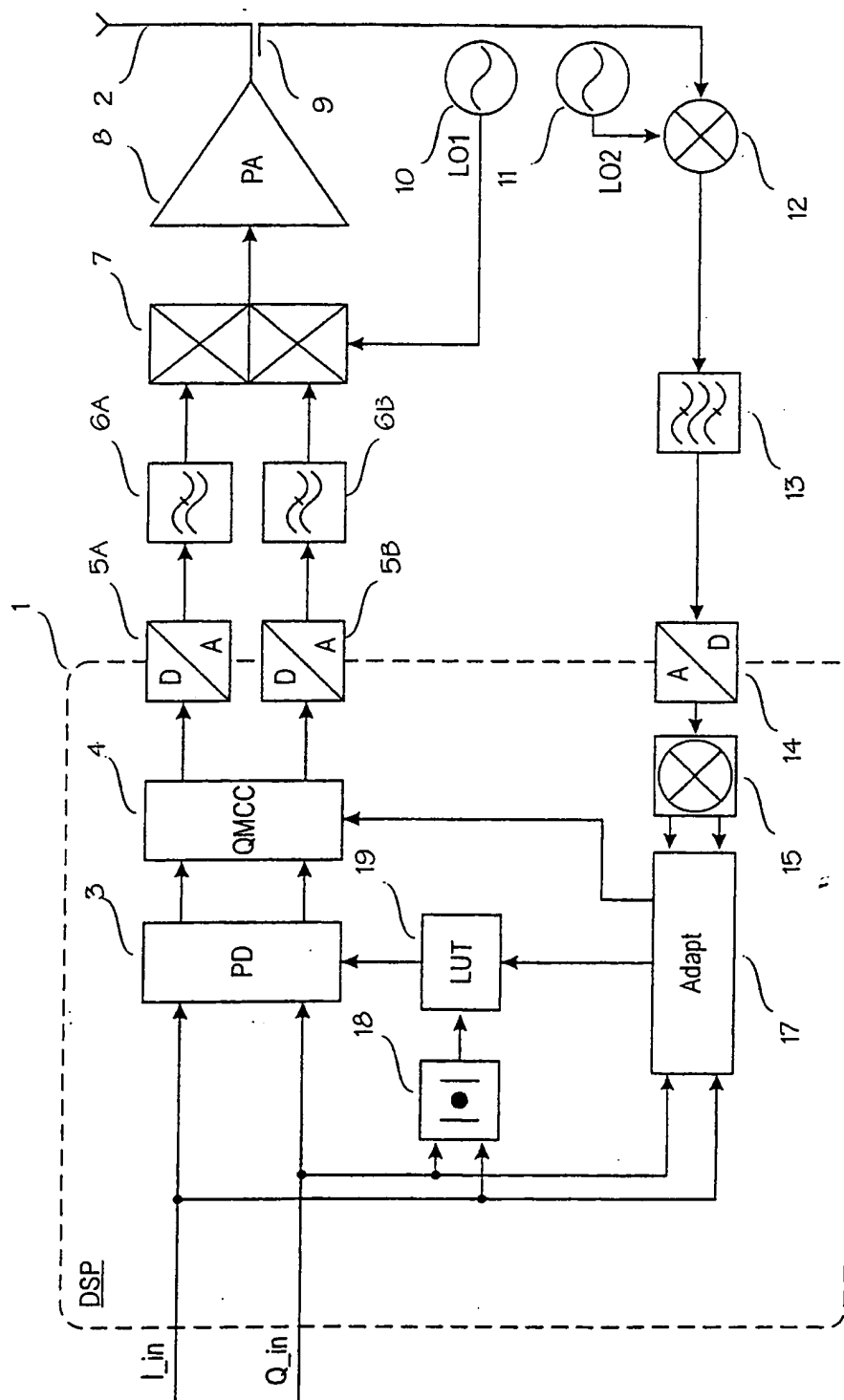
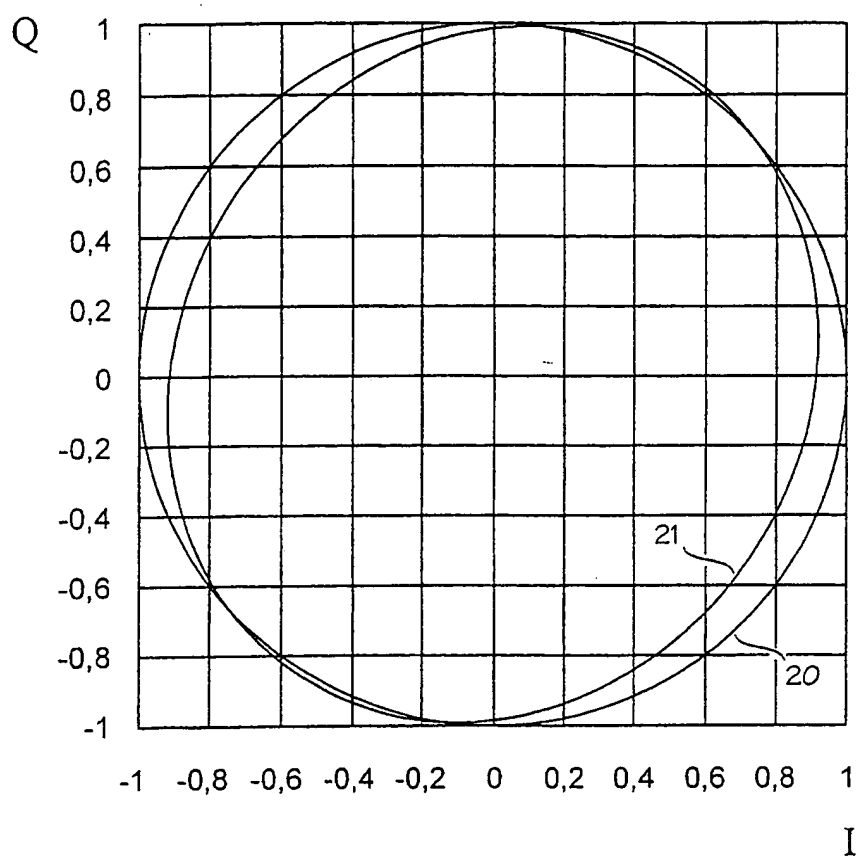


Fig. 2



INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 00/00246

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H04L 27/36

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0503588 A2 (NIPPON TELEGRAPH AND TELEPHONE CORPORATION), 11 March 1992 (11.03.92), page 3, line 26 - line 41, figures 3,4, claims 1-4, abstract	1-10
	--	
X	US 5054037 A (MARTINEAU ET AL), 1 October 1991 (01.10.91), column 1, line 44 - line 63; column 3, line 11 - line 18, figure 4, claim 1, abstract, table 2,3	1-10
	--	
X	US 4926443 A (REICH), 15 May 1990 (15.05.90), column 6, line 55 - column 7, line 14; column 10, line 36 - line 66, figures 4,7, claim 1, abstract	1-10
	--	

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"I" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"I" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

13 July 2000

Date of mailing of the international search report

20-07-2000

Name and mailing address of the ISA:

Swedish Patent Office
Box 5055, S-102 42 STOCKHOLM
Facsimile No. +46 8 666 02 86

Authorized officer

PEDER GJERVALDSAETER/EE
Telephone No. +46 8 782 25 00

INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 00/00246

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4930141 A (OHMAGARI), 29 May 1990 (29.05.90), column 1, line 45 - line 64, claim 1 --	1-10
A	US 5883551 A (MARCHESANI), 16 March 1999 (16.03.99), column 3, line 42 - column 4, line 27, claim 1, abstract --	1-10
A	EP 0598585 A2 (LINEAR MODULATION TECHNOLOGY), 15 November 1993 (15.11.93), claims 1,2, abstract -- -----	1-10

INTERNATIONAL SEARCH REPORT

Information on patent family members

02/12/99

International application No.

PCT/FI 00/00246

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0503588 A2	11/03/92	DE 69222452 D,T JP 5068060 A US 5293406 A	26/02/98 19/03/93 08/03/94
US 5054037 A	01/10/91	DE 68916234 D,T EP 0379114 A EP 0396734 A,B SE 0396734 T3 FR 2641923 A,B JP 2228850 A	02/02/95 25/07/90 14/11/90 20/07/90 11/09/90
US 4926443 A	15/05/90	CN 1038737 A DE 3889326 D EP 0343273 A,B JP 2030237 A JP 2093230 C JP 8002046 B	10/01/90 00/00/00 29/11/89 31/01/90 18/09/96 10/01/96
US 4930141 A	29/05/90	JP 1036153 A JP 1766817 C JP 4053466 B	07/02/89 11/06/93 26/08/92
US 5883551 A	16/03/99	CA 2218925 A EP 0843448 A FR 2755808 A,B JP 10164159 A NO 975224 A	14/05/98 20/05/98 15/05/98 19/06/98 15/05/98
EP 0598585 A2	15/11/93	DE 69324210 D,T GB 2272589 A,B US 5381108 A	05/08/99 18/05/94 10/01/95